

Abstract of the Disclosure

An input changing switch is provided at a preceding stage of a comparator used to measure a DC offset of a D/A converter, and an polarity inverting circuit is provided
5 at a succeeding stage. A first compensation value is generated by a compensation value generating circuit and then stored in a register. A second compensation value is generated by switching the input changing switch and the polarity inverting circuit and then stored in a register.
10 A third compensation value is calculated by averaging the first and second compensation values by means of a compensation value calculating circuit, and then analog output voltages derived based on this compensation value via the offset compensation D/A converter are subtracted
15 from analog output voltages of the main D/A converter. Thus, the DC offset of the D/A converter is compensated.